

Appl. No. 10/645,366
Amdt. Dated 01/12/2006
Reply to Office Action of 08/18/2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	:	10/645,366	Confirmation No. 7488
Applicant	:	Ruban Kanapathippillai, et al.	
Filed	:	08/21/2003	
Title	:	METHODS FOR POWER REDUCTION IN A DIGITAL SIGNAL PROCESSOR INTEGRATED CIRCUIT (As Amended)	
TC/A.U.	:	2111	
Examiner	:	Kenneth S. Kim	
Docket No.	:	42P14037D	
Customer No.	:	8791	

RESPONSE TO OFFICE ACTION

Commissioner for Patents
PO Box 1450
Alexandria VA 22313-1450

Dear Sir:

In response to the Office Action mailed on 08/18/2005, the period for response having been extended with the accompanying petition for extension of time, please enter the following amendments and remarks in the above-identified patent application as follows below:

Amendments to the Claims begin on page 3.

Remarks/Arguments begin on page 7.

Conclusion and signature is on page 10.

42P14037D

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